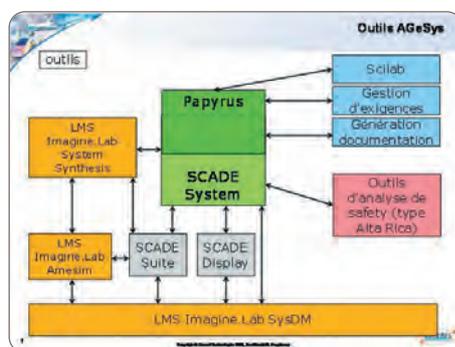


The objective of the AGeSys project is the development of a systems engineering workshop dedicated to embedded systems, for all industrial sectors without restriction. This workshop is made of open, integrated tools that allow a model based design of embedded systems and software, and coupled with multi-physics models. The objective is to allow an industrial process with a clean definition of the system/software/hardware steps.

TECHNOLOGICAL OR SCIENTIFIC INNOVATIONS

- ▶ Allow users from different domains to use system modelization technologies relying on open source COTS, based on the OMG standard SysML, and developed specifically to match the needs of embedded system designers.
- ▶ Integration of complete, coherent and efficient tool chain that covers all activities of system engineering processes, including multi-physics modelling and simulation, software engineering, requirements management, data management, safety analysis, versions and variants management, documentation generation.
- ▶ AGeSys encompasses not only technological innovations, but also a new economical model with a large part of open source components, and is validated by a large set of major french compaignies that covers strategic industrial domains aerospace, automotive and railway.



STATUS - MAIN PROJECT OUTCOMES

- ▶ Development of 3 annual versions of the workshop at the end of each yearly period of the project.
- ▶ Industrial use cases set up by aerospace, automotive and railway mayor players.
- ▶ Dissemination by the french competitiveness poles that are partners of the project through the organization of annual « AGeSys Days ».

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THALES, VALEO

Intermediate size enterprises:
LMS IMAGINE,

SMEs:
ESTEREL TECHNOLOGIES,
SCALEO CHIP, SCILAB

Research institutes, universities:
CEA

PROJECT DATA

Coordinator:
ESTEREL TECHNOLOGIES

Co-label:
AEROSPACE VALLEY

Start date:
January 2012

Duration:
36 months

Global budget (M€):
21.2

Funding (M€):
7.5